

DECLARATION AND POWER OF ATTORNEY

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled: **METHOD FOR FORMING AN INTEGRATED CIRCUIT INTERCONNECT USING A DUAL POLY PROCESS**; the specification of which is attached hereto.

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims.

I acknowledge the duty to disclose information which is material to patentability as defined in Title 37 of the Code of Federal Regulations, Section 1.56.

I hereby claim the benefit under Title 35 of the United States Code, Section 120 to which I am entitled and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35 of the United States Code, Section 112, I acknowledge the duty to disclose information which is material to patentability as defined in Title 37 of the Code of Federal Regulations, Section 1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application.

I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith:

William R. Bachand	Registration No. 34,980
Susan B. Collier	Registration No. 34,566
Angus C. Fox, III	Registration No. 31,828
Kevin D. Martin	Registration No. 37,882
Lia M. Pappas	Registration No. 34,095
David J. Paul	Registration No. 34,692

Send correspondence to: Susan B. Collier, Patent Agent
Micron Semiconductor, Inc.
Patent Department, MS525
P.O. Box 6
Boise, Idaho 83707-0006

Direct phone calls to: Susan B. Collier
208/368-4514

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Title 18 of the United States Code, Section 1001 and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

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Sanh	D.	Tang
(First)	(MI)	(Last)

Sanh D. Tang Date: 03-14-95

United States

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Boise, Idaho United States of America

S/N 08/390,714

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Sanh D. Tang	Examiner:	Cao
Serial No.:	08/390,714	Group Art Unit:	2508
Filed:	February 17, 1995	Docket:	303.451US1
Title:	METHOD FOR FORMING AN INTEGRATED CIRCUIT INTERCONNECT USING A DUAL POLY PROCESS		

REVOCATION AND POWER OF ATTORNEY

Assistant Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

In accordance with 37 C.F.R. Section 1.36, M.P.E.P. Section 402.05, 402.07, and 324 please revoke any existing Powers of Attorney, if any, and appoint the following attorneys and/or patent agents to prosecute this application and to transact all business in the Patent and Trademark Office in connection therewith:

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